
EE/CprE/SE 491 WEEKLY REPORT 05

3/16/2020 – 4/12/2020

Group number: 08

Project title: High Resolution Digitally Trimmable Resistor

Client &/Advisor: Prof. Randy Geiger

Team Members/Role: Clark Reimers - Test Engineer, Pierce Nablo - Design Engineer, Alek Benson - Information Manager, Oluwatosin Oyenekan - Meeting Lead

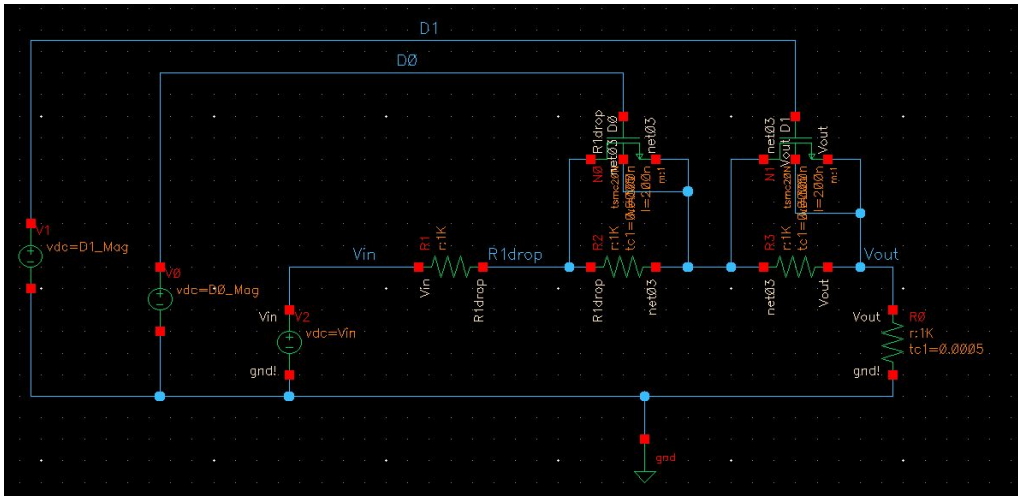
❖ **Weekly Summary**

During the past 4 weeks our group had to tackle many issues related to going remote. We went remote due to Iowa State shutting campus down because of the COVID-19 situation. Therefore during the first few weeks of this period we migrated our entire structure to an online environment including access to Virtuoso, access to shared files, and meetings. Once all of the bugs were worked out with remote access, we then began to simulate the temperature coefficient of a resistor and a resistor divider structure. While doing so we ran into issues where the simulation environment would manipulate our temperature coefficients to a function dependent on temperature but we wanted it to be represented by a constant, independent of temperature.

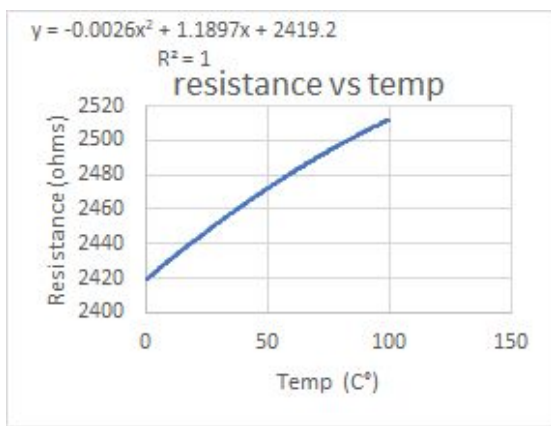
❖ **Past week accomplishments**

Clark Reimers:

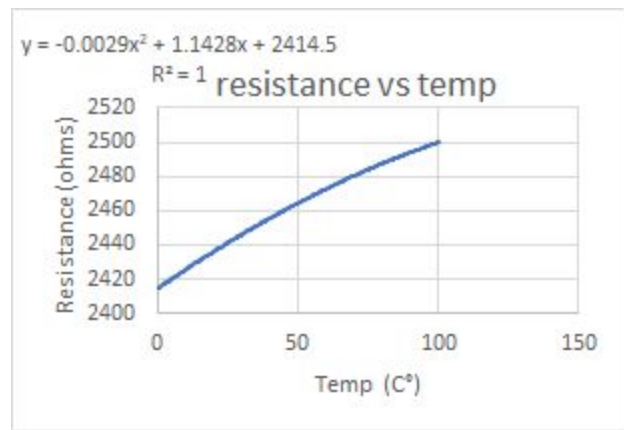
- General troubleshooting with the changeover to online everything.
- Assisted team members with technical troubleshooting
 - RDP issues, VPN issues, Virtuoso issues, etc.
- Assisting with finding the temperature coefficient for the single resistor and took it to simulations.
 - Troubleshooting the temperature coefficient not being a constant with our testing.
 - Determined that the equation Virtuoso used was different than the ones we were using for calculations.
- Assisted with finding the temperature coefficient equations for the resistor divider structure in its 4 different states after establishing the coefficient for the resistor.



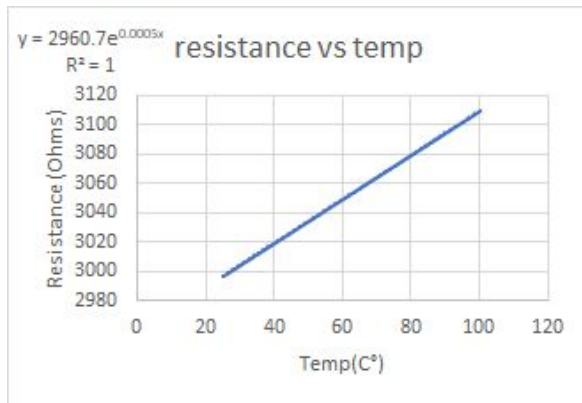
Resistor divider structure



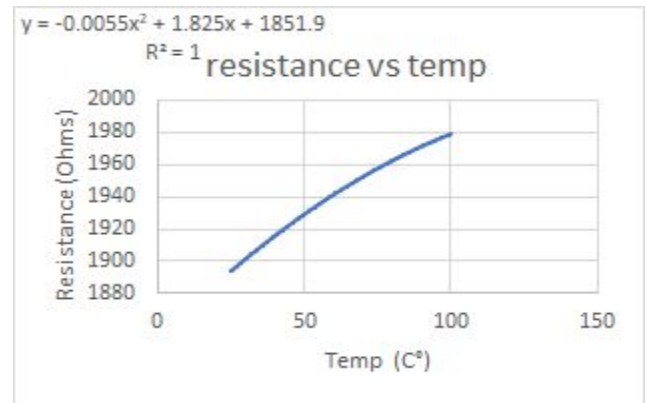
D0 on, D1 off



D0 off, D1 on



D0 off, D1 off

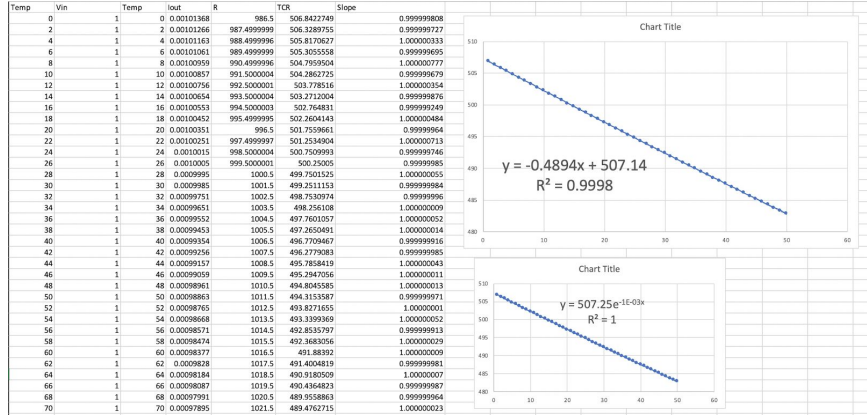
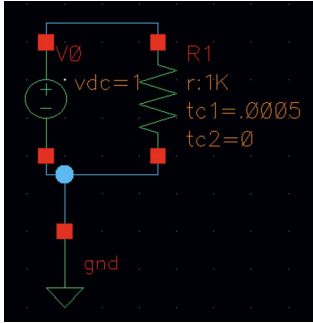


D0 on, D1 on

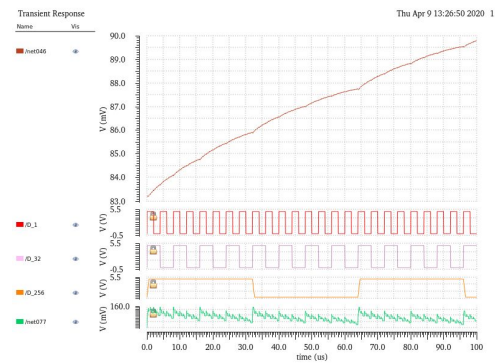
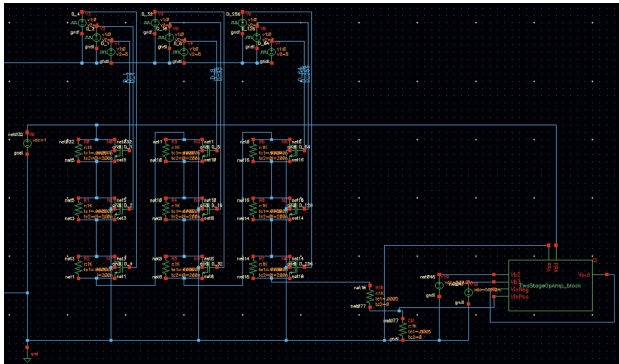
- Started working on expanding to larger scale circuits
- Presented with my group to Dr.Geiger
- Completed weekly assignments and participated in Lightning take

Pierce Nablo:

- Worked on getting vpn to work
- Worked on getting remote access to work
- Troubleshooted issues with running Virtuoso remotely
- Troubleshooted issues with getting SPECTER to run remotely
- Simulated a basic resistor to debug why our temperature coefficient is not acting as a constant.



- Made a large series resistor design to have a better reference to compare other designs too for the future.

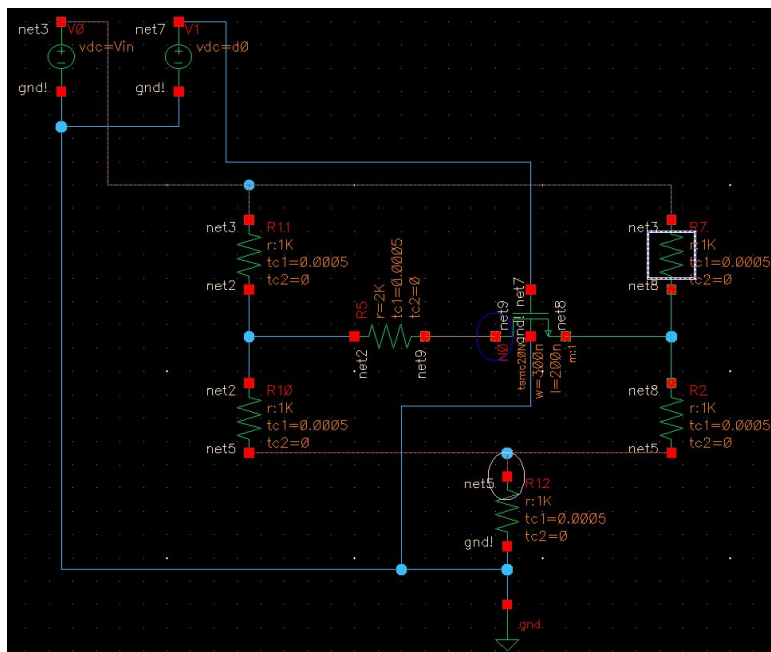


- Presented to Dr. Geiger the simulation results
- Made the Lightning talk video
- Completed weekly assignments for EE491

Alek Benson:

- Continued reviewing 330/435 material.
- Continued researching different trimming technologies
- Worked on setting up a Virtuoso environment on a remote desktop.
- Worked on getting Cadence software implemented in Virtuoso
- Worked on getting a sharepoint library through one drive to hold some excel data files.
- Worked on the simulating temperature coefficient of the series resistor trimming structure.
- Worked on the schematics for the ladder structure and simulating in Cadence.
- Worked on the Temperature Coefficient formula in Cadence and resolving the real life model of Temperature Coefficient of a basic resistor.

/net1 X	/net1 Y	/R0/PLUS	/R0/PLUS	res	TCR	TCR2
25	1	25	0.001001	999	500.5005	500
25.5	1	25.5	0.001001	999.25	500.3753	500
26	1	26	0.001001	999.5	500.2501	500
26.5	1	26.5	0.001	999.75	500.125	500
27	1	27	0.001	1000	500	500
27.5	1	27.5	0.001	1000.25	499.875	500
28	1	28	0.001	1000.5	499.7501	500
28.5	1	28.5	0.000999	1000.75	499.6253	500
29	1	29	0.000999	1001	499.5005	500
29.5	1	29.5	0.000999	1001.25	499.3758	500
30	1	30	0.000999	1001.5	499.2511	500
30.5	1	30.5	0.000998	1001.75	499.1265	500
31	1	31	0.000998	1002	499.002	500
31.5	1	31.5	0.000998	1002.25	498.8775	500
32	1	32	0.000998	1002.5	498.7531	500
32.5	1	32.5	0.000997	1002.75	498.6288	500
33	1	33	0.000997	1003	498.5045	500
33.5	1	33.5	0.000997	1003.25	498.3803	500
34	1	34	0.000997	1003.5	498.2561	500
34.5	1	34.5	0.000996	1003.75	498.132	500
35	1	35	0.000996	1004	498.008	500
35.5	1	35.5	0.000996	1004.25	497.884	500
36	1	36	0.000996	1004.5	497.7601	500
36.5	1	36.5	0.000995	1004.75	497.6362	500
37	1	37	0.000995	1005	497.5124	500
37.5	1	37.5	0.000995	1005.25	497.3887	500
38	1	38	0.000995	1005.5	497.265	500



Oluwatosin Oyekan:

- Transitioned to working online
- Set up remote desktop, figuring out how to get Virtuoso to work from my home computer
- Did some research on how to find the temperature coefficient of a circuit.
- Researched how the ladder structure would be able to trim a resistor and tried to understand the math behind it
- Didn't run any simulations myself, wanted to see the appropriate approach from my team members to finding temperature coefficient
- Completed weekly assignments for EE491

❖ **Pending issues**

Clark Reimers:

- Have had several issues nailing down precise temperature coefficients for our circuits. We still need to find an equation for resistance.

Pierce Nablo:

- All issues that were encountered over the past 4 weeks have been dealt with.

Alek Benson:

- No issues

Oluwatosin Oyekan:

- No Issues

❖ **Individual contributions**

<u>Name</u>	<u>Hours 3/16 - 3/29</u>	<u>Hours 3/30 - 4/12</u>	<u>Hours cumulative</u>
Clark Reimers	12	10	72
Pierce Nablo	6	14	71
Alek Benson	13	12	72
Oluwatosin Oyekan	7	10	65

❖ Plans for the upcoming week

Clark Reimers: Moving forward, we need to continue with the goal of defining a precise temperature coefficient for the current schematics that we have. For this, we need to define equations for resistances and the TCR equations. We also need to continue with expanding to larger structures and do some research on whether or not the temperature coefficient is constant. We also need to fully understand how our simulation environment is working so that there is no ambiguity in our testing.

Alek Benson: The plan is to finally fix the model for temperature coefficient of resistance, and find a way to correctly input this into Cadence. Once this is done, I can spend time re-running simulations of the series structure. I will also spend time simulating the ladder resistor structure and document my findings about it. I plan to spend some time modifying the schematics of the ladder structure to understand the trimming capabilities better.

Oluwatosin Oyekan: The plan for this week is to begin simulating circuits to aid find the coefficient. the design ideas we came up with and present it to Gieger. My goal for this period is to test out and get a positive result from at least one circuit.

Pierce Nablo: For the next week, I want to dig into a physics textbook in order to figure out the definition of resistivity to help me understand why the simulation environment is behaving the way it. Then I will need to figure out what SPECTER needs from me in order to get the temperature coefficient to be a constant.

❖ Summary of weekly advisor meeting

We had numerous meetings with our academic advisor these past two periods for senior design. Our initial meeting was fine, as we updated the advisor with our move to online schooling as well as working on migrating to online methods to continue with our project. We talked about the next steps of the project and the need to examine the data and understand that Cadence simulations are ideal models and more tuning of the software is required to fit the simulation to our realistic model. We continued to update him for a couple of weeks every couple of days and tried to find what Cadence uses for temperature coefficient modeling. During the next period, we spent time trying to find the true model for temperature coefficient because it is a non constant property despite many resources listing it as a constant value. Our advisor is adamant about fixing this issue before continuing with more advanced simulations. The meetings for this period weren't great, but we have a direction to head in.